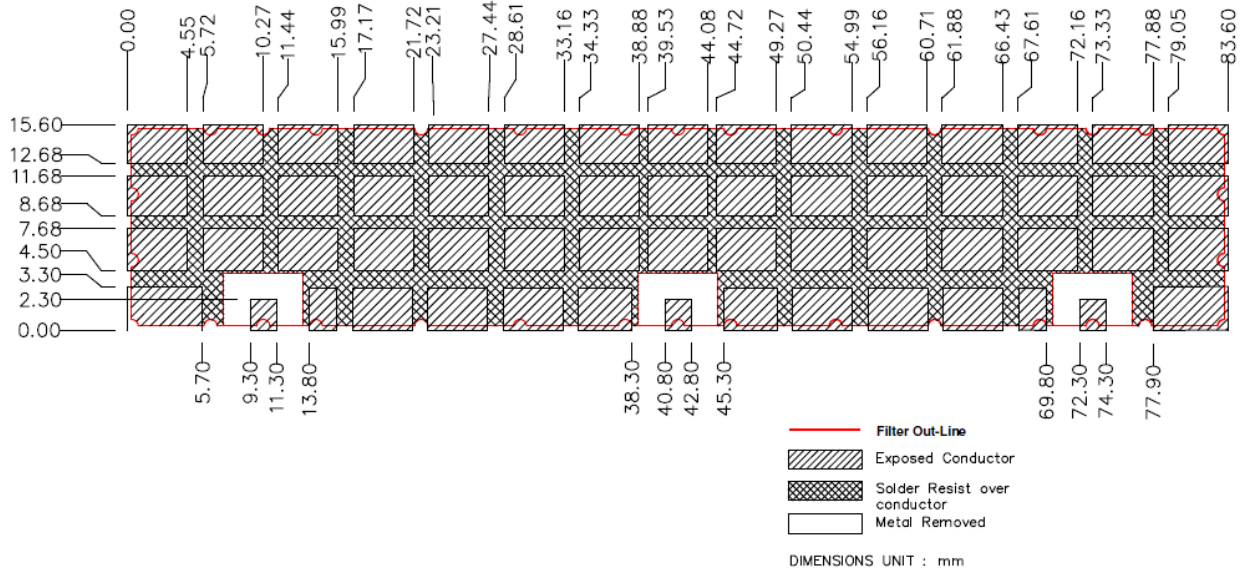


Recommended PCB Layout



Electrical Response

